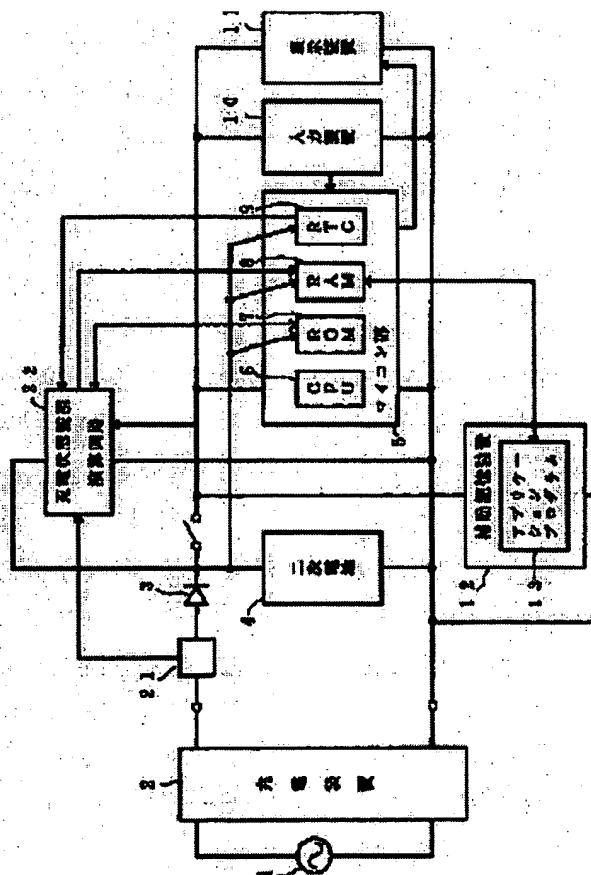


SECONDARY BATTERY CHARGED STATE MONITORING SYSTEM IN ELECTRONIC APPARATUS

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Inventor: UENO HIROYASU
Applicant: SHARP CORP
Classification:
 - international: H01M10/48
 - european:
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Abstract of JP6163083

PURPOSE: To precisely grasp the charged state of a secondary battery by software even in the state where a power source is cut.
CONSTITUTION: A charging connection detecting circuit 21 is provided between a charging device 2 and a secondary battery 4, and a charging state monitoring arithmetic circuit 22 for receiving power supply from the secondary battery 4 is provided. The charging state monitoring arithmetic circuit 22 counts the clock from the real time clock 9 of a microcomputer part 5 when the charging connection detecting circuit 21 is laid in the detecting state, and stores the charging time t_c in a timer counter TCc in the RAM 8 of the microcomputer part 5. At power source input, the present charge residual capacity Q_0 is calculated from the charge residual quantity Q_0 at power source cut, the charge time t_c and the charged current value I_c by use of an application program 13, and displayed on a display device 11.



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